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DB: USPAT;EPD;JPO

Default operator: OR

☒ Plurals

☒ Highlight all hit terms initial

((packag\$3 and magnetic adj shield\$3) and (die chip)) and substrate) and mold\$3

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6487864 B1	20021203	16	Cryogenic inertial micro-electro-mechanical	62/3.2	62/51.1
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6452253 B1	20020917	8	Method and apparatus for magnetic shielding of an	257/659	257/687
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6429044 B1	20020806	8	Method and apparatus for magnetic shielding of an	438/106	438/127
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6404317 B1	20020611	86	Planar magnetic element	336/200	336/232; 336/83
5	<input type="checkbox"/>	<input type="checkbox"/>	US 6300617 B1	20011009	28	Magnetic digital signal coupler having	250/214.1	250/551; 324/252
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6249824 B1	20010619	151	Magnetic data storage fixed hard disk drive using	710/8	360/97.01
7	<input type="checkbox"/>	<input type="checkbox"/>	US 6249039 B1	20010619	17	Integrated inductive components and method of	257/531	257/528; 257/549;
8	<input type="checkbox"/>	<input type="checkbox"/>	US 6081416 A	20000627	8	Lead frames for mounting ceramic electronic parts,	361/308.1	361/321.2; 361/813
9	<input type="checkbox"/>	<input type="checkbox"/>	US 5866942 A	19990202	23	Metal base package for a semiconductor device	257/698	257/678; 257/778
10	<input type="checkbox"/>	<input type="checkbox"/>	US 5849355 A	19981215	16	Electroless copper plating	427/79	29/602.1; 333/167;
11	<input type="checkbox"/>	<input type="checkbox"/>	US 5811050 A	19980922	14	Electromagnetic interference shield for electronic	264/299	264/164; 264/231;